

Case Docket No. ASMJP.062AUS.

Date: April 9, 2003

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Mitsusuke Kyogoku, et al. In re application of: 09/650,122 App. No. August 29, 2000 Filed

SEALING MECHANISM OF For MULTI-CHAMBER LOAD-

LOCKING DEVICE

Examiner

Ram N. Kackar

1763 Art Unit

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: United States Patent and Trademark Office, P.O. Box 2327, Arlington, VA 22202, on

April 9, 2003

Katsuhiro Arai, Reg. No. 43,315

UNITED STATES PATENT AND TRADEMARK OFFICE P.O. Box 2327

Arlington, VA 22202

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TC 1700

Sir:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated as shown below:

		CL	AIMS AS FILED			
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims Independent Claims	4	<u> </u>	20	= 0 ×	\$18	= \$0
	1		3	= 0 ×	\$84	= \$0
				TOTAL ADI	DITIONAL F MENDMEN	EE T \$0

- Amendment in seven (7) pages. (X)
- Return prepaid postcard. (X)
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N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Kyogoku et al.)	Group Art Unit 1763
Appl. No.	:	09/650,122)	
Filed	:	August 29, 2000)	
For	:	SEALING MECHANISM OF MULTI-CHAMBER LOAD- LOCKING DEVICE)	The same of the sa
Examiner	:	R. Kackar	_)	APR 16 2003 TC 1700
		AMENDMENT		TC 1700

AMENDMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Dear Sir:

In response to the Office Action mailed July 22, 2002 (Paper number 6), please amend the above-captioned application as follows:

IN THE CLAIMS:

Please cancel Claims 3, 6, and 11 without prejudice.

Please amend Claim 1 as follows:

Claim 1 has been amended as follows:

1. (Thrice amended) A multi-chamber load-locking device for transferring wafers, said device having an interior divided into (i) an upper chamber and (ii) a lower chamber, and (iii) an intermediate section located between the upper chamber and the lower chamber, which is for loading/unloading wafers,

said device comprising (a) a single divider plate having an upper side and a lower side, both of which are for temporarily supporting wafers, said plate moving reciprocally between an upper position and a lower position, wherein the plate divides and seals the upper chamber from the intermediate section and the lower chamber at the upper position, and the plate divides and